

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Ki-Young LEE et al.

Examiner: P. Brock II

Serial No: 09/389,491

Art Unit: 2815

Filed: September 3, 1999

For: CAPACITOR OF SEMICONDUCTOR INTEGRATED CIRCUIT AND ITS
FABRICATING METHOD

TECHNOLOGY CENTER 2330
#12
112-01
Howe

PETITION FOR EXTENSION OF TIME
UNDER 37 C.F.R. § 1.136

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

It is respectfully requested that an extension of time for the period indicated below be granted in accordance with the provisions of 37 C.F.R. §1.136 to take the action required in the application identified in caption, as reflected by the papers submitted herewith.

<u>X</u> First Month	\$110 (\$ 55)*
<u> </u> Second Month	\$290 (\$145)*
<u> </u> Third Month	\$520 (\$260)*
<u> </u> Fourth Month	\$520 (\$260)*
<u> </u> Fifth Month	\$500 (\$250)*

*(Small Entity) TOTAL FEE: \$110.00

The Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 5-01645. If one or more (additional) extension(s) of time is/are required for the filing of this paper, such extension(s) is/are hereby expressly petitioned for and the Commissioner is authorized to charge the required fee to Deposit Account No. 5-01645.

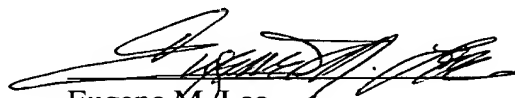
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Respectfully submitted,

November 13, 2001
Date


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